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SUN MICROSYSTEMS, INC. hereby revokes any previous Powers of Attorney and appoints the following attorneys and/or agents in connection with the patent applications and patents identified in Appendix A, owned by SUN MICROSYSTEMS, INC., that are filed with the United States Patent and Trademark Office:

Andrew C. Chen	Reg. No. 43,544	Elaine K. Lee	Reg. No. 41,936
Bernice B. Chen	Reg. No. 42,403	Sean P. Lewis	Reg. No. 42,798
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Marilyn E. Glaubensklea	Reg. No. 35,521	Monica D. Lee	Reg. No. 40,696
Anirra R. Gupta	Reg. No. 38,275	Joseph A. Twarowski	Reg. No. 42,191
Noreen A. Krall	Reg. No. 39,734		

each of said attorneys being employed by Sun Microsystems; and

Mark K. Brightwell	Reg. No. 47,446	Lawrence J. Merkel	Reg. No. 41,191
Kay A. Colapret	Reg. No. 52,759	Eric B. Meyertons	Reg. No. 34,876
Stephen J. Curran	Reg. No. 50,664	Neal E. Persky	Reg. No. 53,452
Mark R. DeLuca	Reg. No. 44,649	Liza Philip	Reg. No. 51,352
Heather L. Flanagan	Reg. No. 54,101	David W. Quimby	Reg. No. 39,338
Russell Henrichs	Reg. No. 50,354	Rory D. Rankin	Reg. No. 47,884
Erik A. Heter	Reg. No. 50,652	Gareth Sampson	Reg. No. 52,191
Jeffrey C. Hood	Reg. No. 35,198	Chris Thompson	Reg. No. 43,188
B. Noël Kivlin	Reg. No. 33,929	Mark S. Williams	Reg. No. 50,658
Robert C. Kowert	Reg. No. 39,255		

each of said attorneys or agents being a member or an associate of the firm of Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., as its attorney or agent for so long as they remain with such firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Send all future correspondence to:

B. Noël Kivlin
Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, Texas 78767-0398
(512) 853-8800

Assignee of Interest

SUN MICROSYSTEMS, INC.
4150 Network Circle
Santa Clara, California 95054

Dated: March 29, 2004

By: Paul D. Sorkin
Paul D. Sorkin, Reg. No. 39,039
Assistant General Counsel

2183



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Whay S. Lee

Serial No.: 09/967,137

Filing Date: September 28, 2001

For: Mapping of Nodes in an
Interconnection Fabric

§ Atty. Dkt. No.: 5681-80900

§

§ Examiner: Unknown

§

§ Art Unit: 2183

§

§

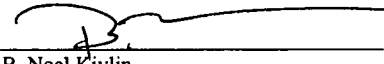
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Date



B. Noel Kivlin

Registered Representative

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Sir:

Please find attached a Power of Attorney with regard to the above-identified patent application. Applicant respectfully requests the Commissioner to change the correspondence address for the above identified patent application. The old correspondence address was:

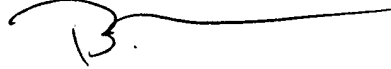
Konrad Raynes Victor & Mann, LLP
315 South Beverly Drive
Suite 210
Beverly Hills, CA 90212

The new correspondence address is:

B. Noël Kivlin
Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, Texas 78767-0398
(512) 853-8840

If there are any questions regarding this matter, please contact me at the telephone number provided below.

Respectfully submitted,



B. Noël Kivlin
Registration No. 33,929

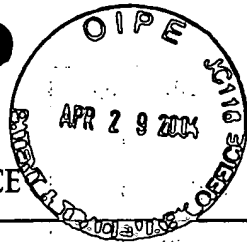
Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, Texas 77253-3267
(512) 853-8840
Date: 4/26/04

APPENDIX A

MHKKG Ref	Sun Ref	Filing Date	Serial #	Title	Inventor(s)
56000	P4637	11/21/00	09/721,029	Single Unit Office Management Center	Talagala, Whay, Wu, Rettberg
37100	P4244PSP	10/30/00	60/244,463	Array of IDE/ATA Disks Emulating SCSI or Fibre Channel Devices	Yatziv
79600	P5480	12/28/00	09/750,555	Method, System, and Program Managing the Rate at Which Client Requests for a Service are Transmitted	Conner
79700	P5624	04/04/01	09/826,081	Method, System, and Program for Enabling Communication Between Devices Using Different Address Formats	Rao
79800	P5781	01/30/01	09/772,626	Method, System, Program, And Data Structures For Testing A Network System Including Input/Output Devices	DeRolf & Kirton
79900	P5786	03/08/01	09/802,229	Method, System, and Program for Determining System Configuration Information	Albright, DeRolf, Gibson, Kirton, & McKenney
80000	P5791	04/04/01	09/826,080	Method, System, and Program for Enabling Communication Between Devices Using Dynamic Addressing	Rao
80100	P5883	03/21/02	10/104,923	Message Routing in a Torus Interconnect	Lee, Talagala, Chong, Wu, & Rettberg
80800	P6196	09/28/01	09/967,141	XOR Processing Incorporating Error Correction Code Data Protection	Acton
80900	P7057	09/28/01	09/967,137	Mapping of Nodes in an Interconnection Fabric	Whay S. Lee
81000	P8430	05/15/03	10/440,218	Method, System, and Program for Allocating Storage Resources	Kumar & Taylor
81100	P8992	03/13/03	10/389,642	Method, System, and Article of Manufacture for Fault Determination	Gibson, McKenney, Cadieux, & Kiser
81200	P6555	06/13/02	10/172,302	System and Method for Isolating Faults in a Network	Wiley, Westminster, & Cadieux
81300	P6248PSP	06/15/01	60/298,658	SAN First Fault Isolation	Wiley, Munson, & Cadieux
81301	P6248	06/13/02	10/172,303	System and Method for Rapid Fault Isolation in a Storage Area Network	Wiley, Munson, & Cadieux
81400	P8966	11/04/03	10/702,150	Using and Generating Finite State Machines to Monitor System Status	Keyur B. Desai
81500	P5445	08/10/01	09/927,985	Method, System, and Program for Managing Multiple Resources in a System	Carlson & da Silva
81600	P5533	04/30/01	09/846,801	Data Integrity Monitoring Storage System	Talagala
81700	P5826	09/28/01	09/967,142	Discovery of Nodes in an Interconnection Fabric	Lee & Mortensen
81800	P7513PSP	02/05/02	60/355,611	Integrated and Automated Storage Management	Amy Lynch
81801	P7513	02/04/03	10/357,554	Method, System, and Program for Supporting a Level of Service for an Application	Amy Elizabeth Lynch & Mark A. Carlson
81900	P5866	06/29/01	09/896,507	Method and Apparatus for Fault Isolation on Network Loops	Gavin Gibson, Sam Gibson, & Yuan Zhang
82000	P7200	06/29/01	09/896,662	Method and Apparatus for Fault Isolation on Network Loops Using Low Level Error Counters	Gavin Gibson, Sam Gibson, & Yuan Zhang



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DECEMBER 11, 2001

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SUN MICROSYSTEMS, INC.
HUGH H. MATSUBAYASHI, ESQ.
M/S: PAL01-521, 901 SAN ANTONIO RD.
PALO ALTO, CALIFORNIA 94303



101871310A

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REEL/FRAME: 012228/0475
NUMBER OF PAGES: 4

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

LEE, WHAY S.

DOC DATE: 09/28/2001

ASSIGNEE:

SUN MICROSYSTEMS, INC.
901 SAN ANTONIO ROAD
M/S: PAL01-521
PALO ALTO, CALIFORNIA 94303

SERIAL NUMBER: 09967137
PATENT NUMBER:

FILING DATE: 09/23/2001
ISSUE DATE:

MARCUS KIRK, EXAMINER
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Hugh H. Matsubayashi, Esq.

Address (line 1)

Sun Microsystems, Inc.

Address (line 2)

901 San Antonio Road

Address (line 3)

M/S: PAL01-521

Address (line 4)

Palo Alto, California 94303

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Hugh H. Matsubayashi, Reg. 43,779
Name of Person Signing

Signature

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Hugh H. Matsubayashi 9-28-2001

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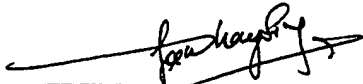
SELL, ASSIGN AND TRANSFER to **SUN MICROSYSTEMS, INC.** (the Assignee), a corporation organized and existing under and by virtue of the laws of the State of **Delaware**, and having its principal place of business at **901 San Antonio Road, Palo Alto, California 94303** their entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith, and is entitled, **MAPPING OF NODES IN AN INTERCONNECTION FABRIC**, such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;

COVENANT AND AGREE that the above is and will be binding on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

By:  Date: Sep 28, 2001
Whay S. Lee

Hugh H. Matsubayashi
Registration Number: 43,779
Sun Microsystems, Inc.
901 San Antonio Road
MS: UPAL01-521
Palo Alto, CA. 94303

Atty Dkt No. P7057 US

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Area Code and Telephone Number (510) 936-2733

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Name of Person Signing

Signature

Date

Hugh H. Matsubayashi 9-28-2001